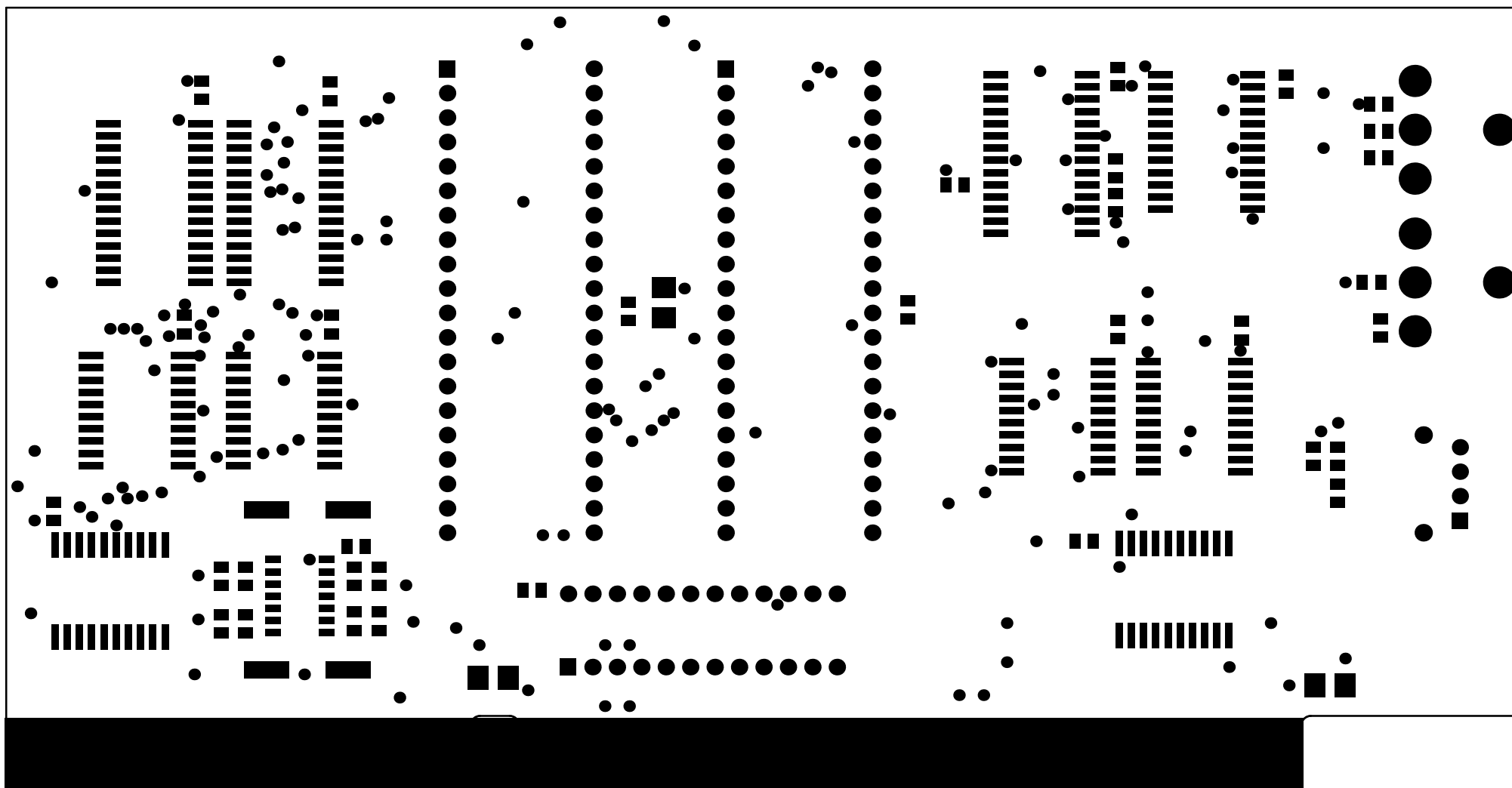


cdio

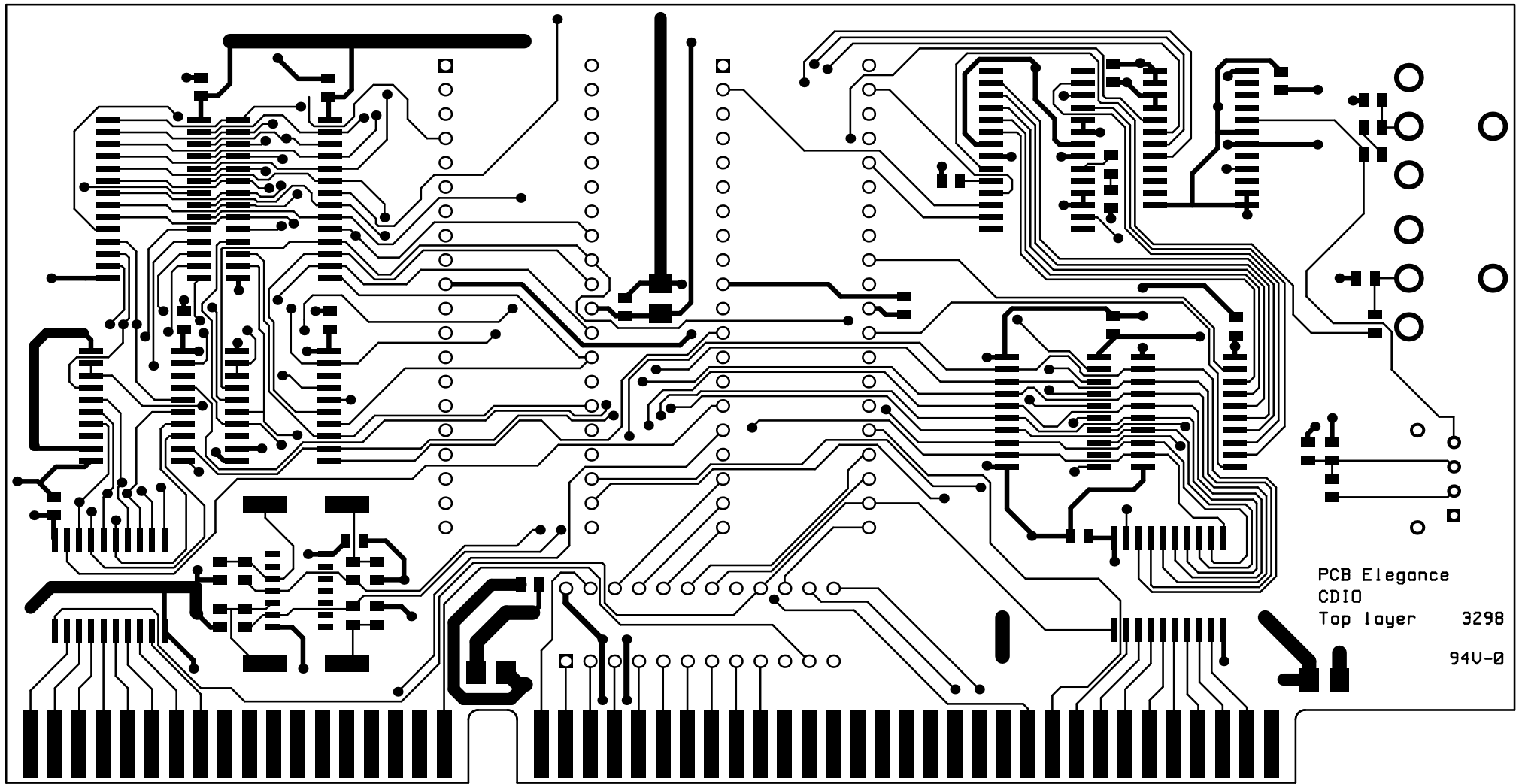
Paste mask top

Apr 29, 2012

14:38:01



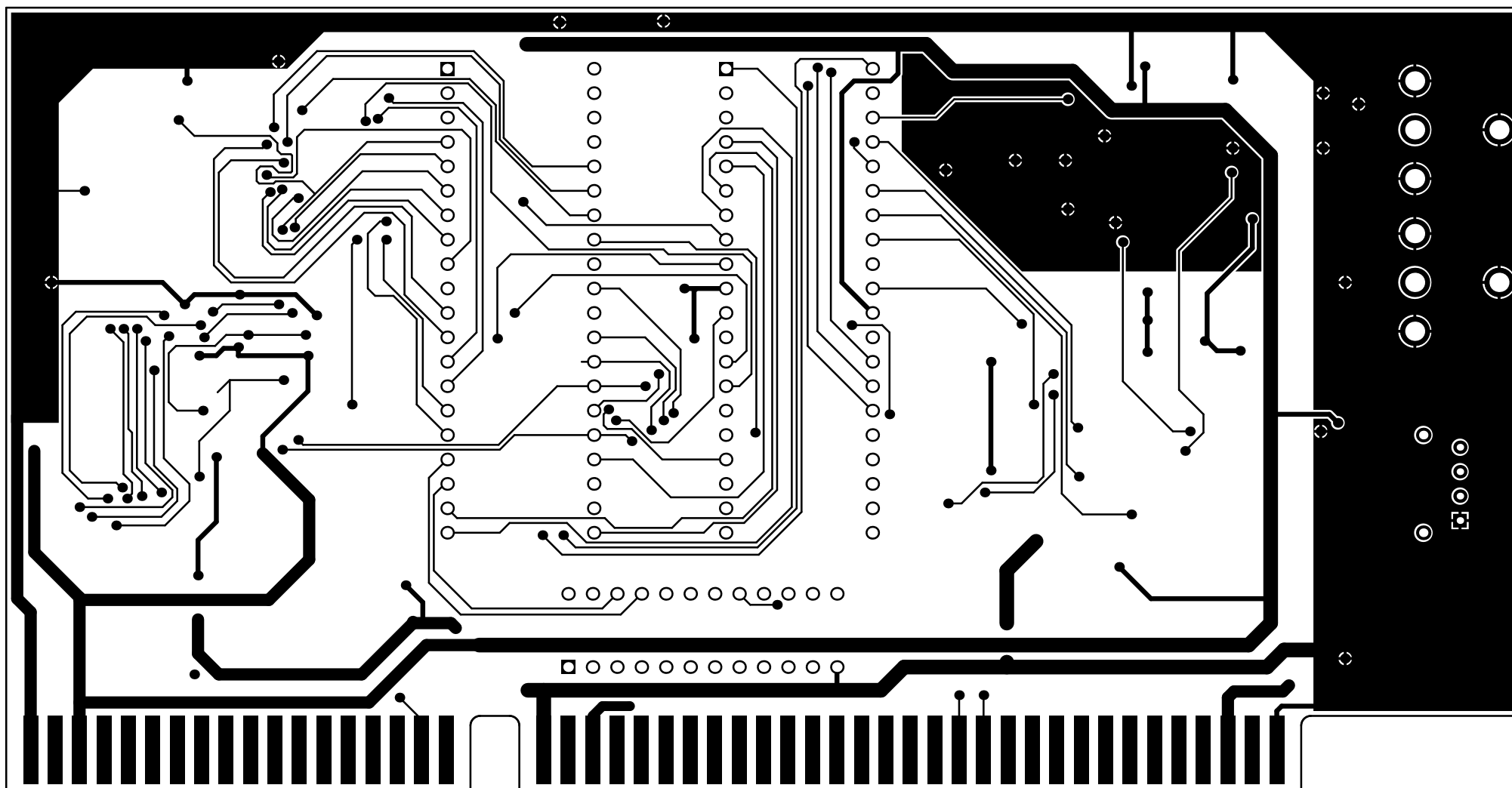
cdio  
Solder mask top  
Apr 29, 2012 14:38:01



PCB Elegance  
CDIO  
Top layer 3298  
94U-0

cdio  
Layer 1 (Top)  
Apr 29, 2012

14:38:01

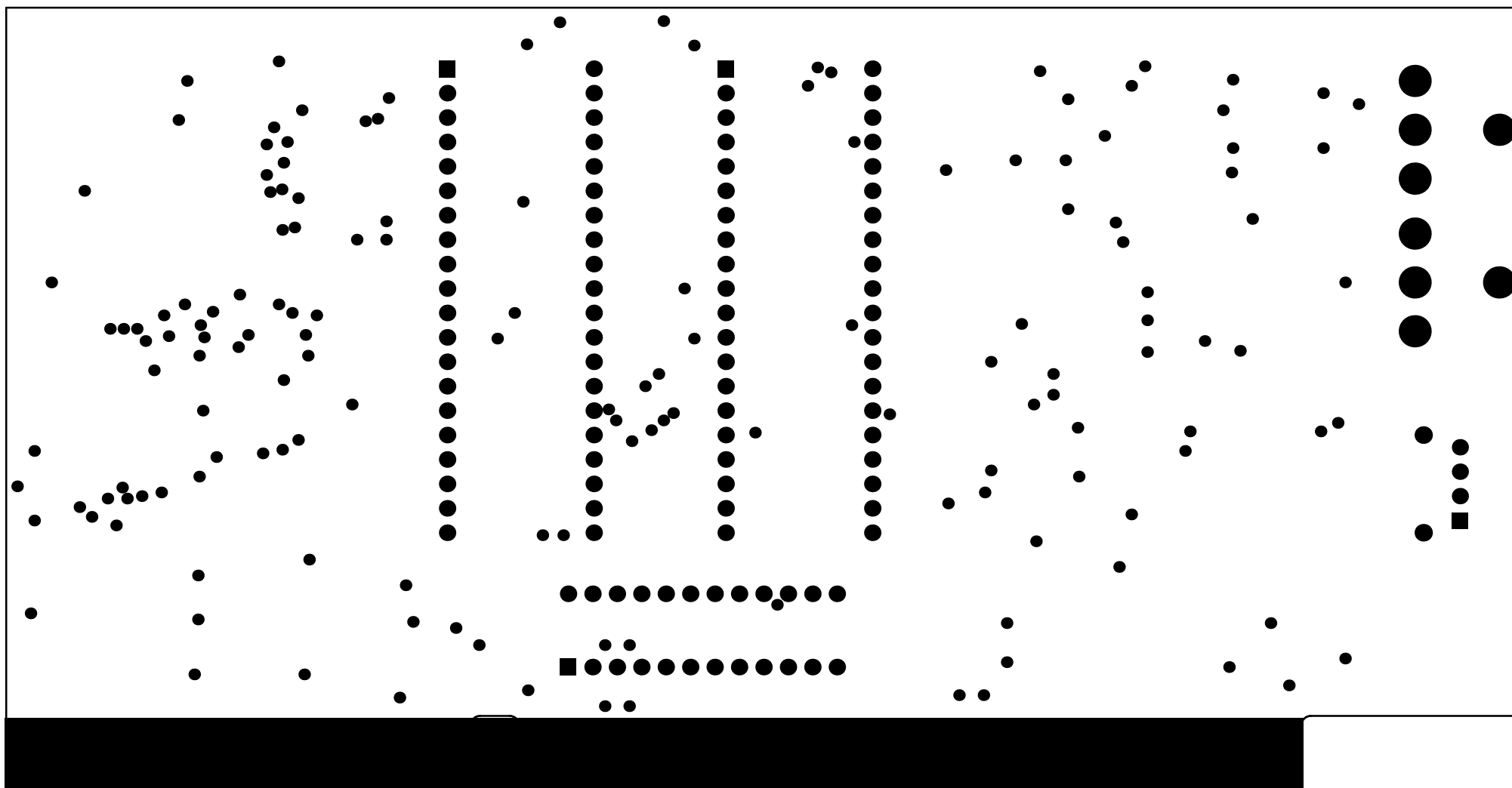


cdio

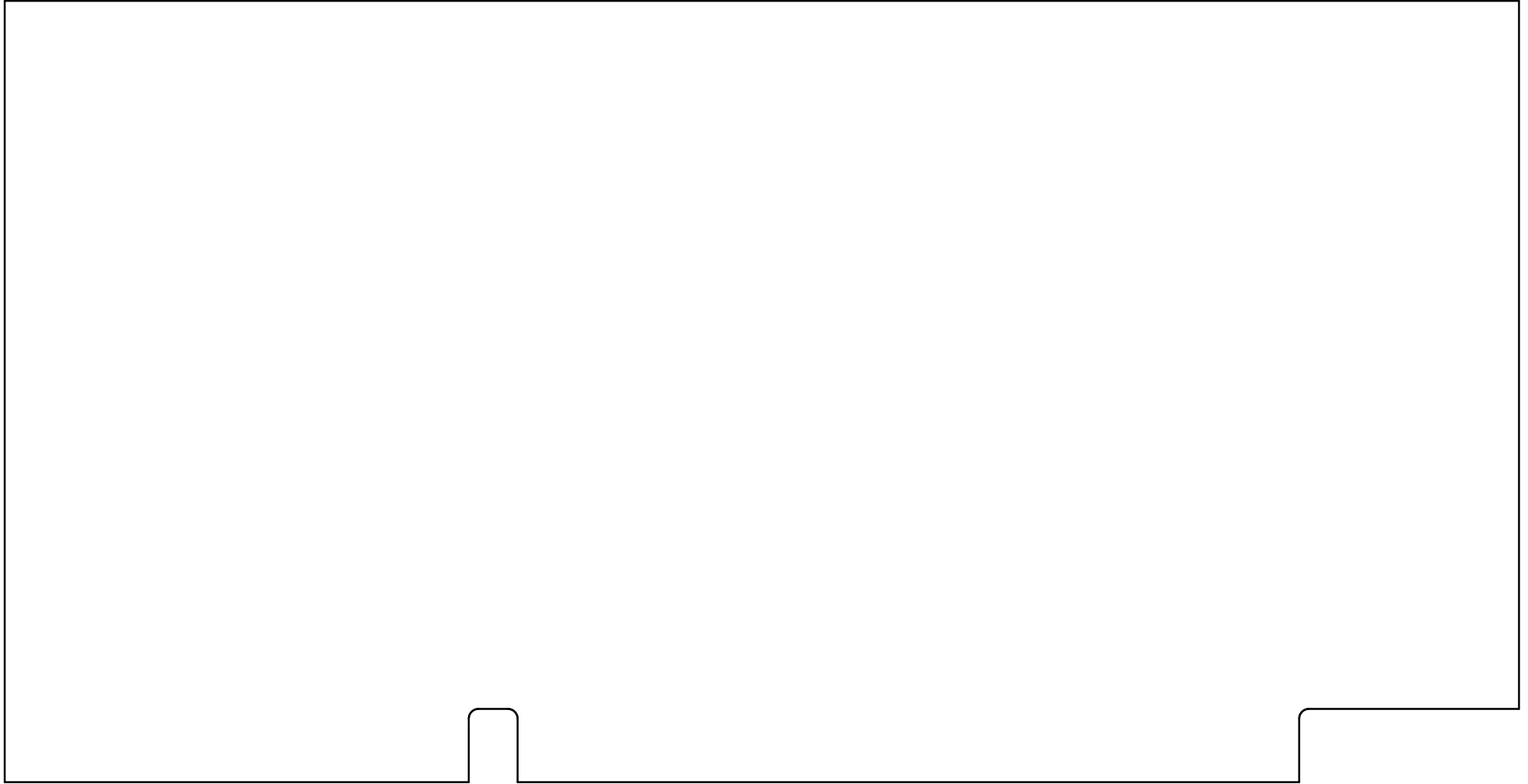
Layer 2 (Bottom)

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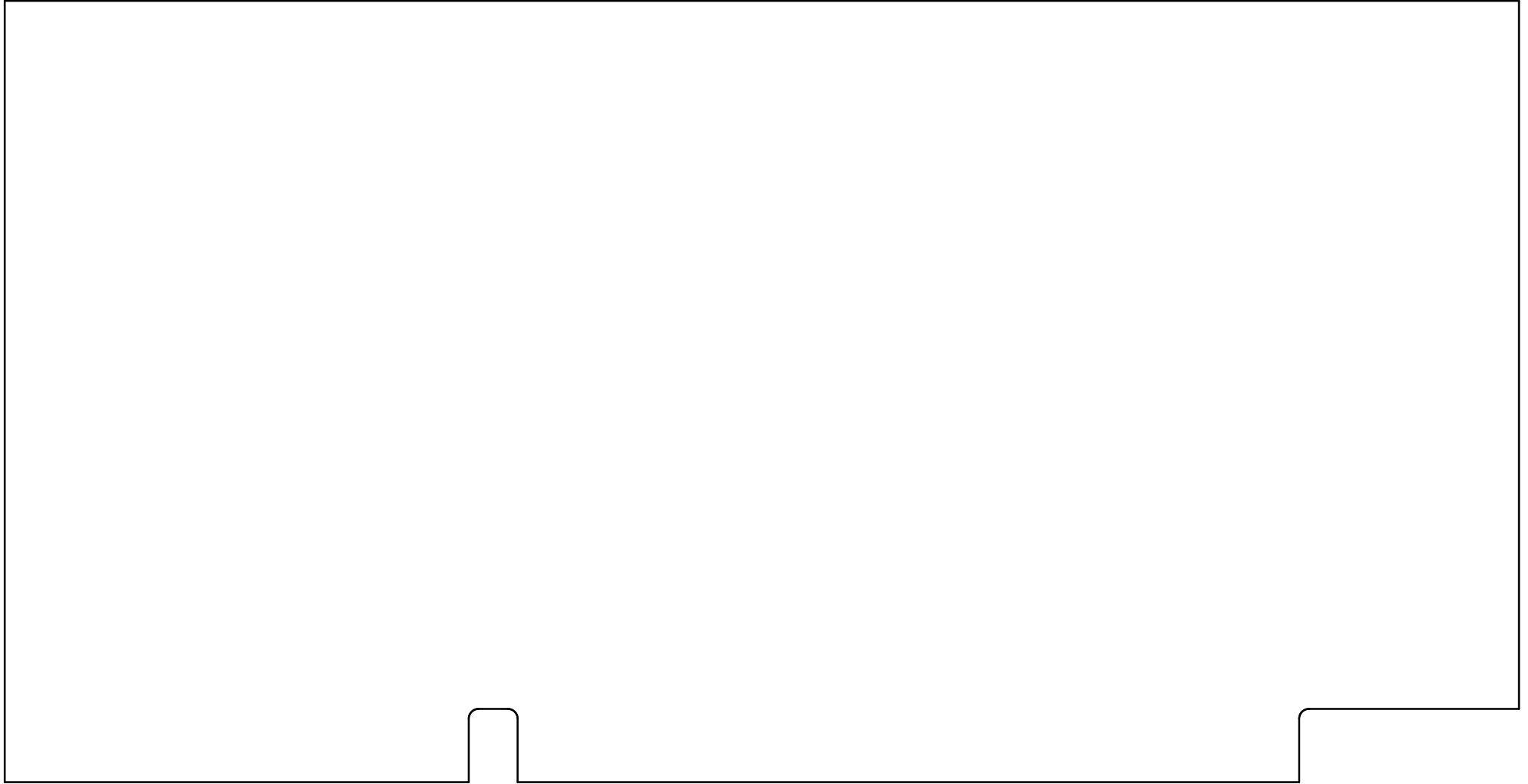
14:38:01



cdio  
Solder mask bottom  
Apr 29, 2012 14:38:01



cdio  
Paste mask bottom  
Apr 29, 2012 14:38:01



cdio  
Silkscreen bottom (refs)  
Apr 29, 2012 14:38:01



